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PATENT APPLICATION

ATTORNEY DOCKET NO. 200302257-2

Inventor(s): Kourosh Gharachorloo et al

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Application No.: 10/672960

Examiner: Mardochee Chery

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Group Art Unit: 2188

Title: **System And Method For Limited Fanout Daisy Chaining Of Cache Invalidation Requests In A Shared-memory Multiprocessor System**

Commissioner For Patents
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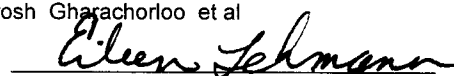
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Respectfully submitted,

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Date : December 6, 2007

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